

L Number	Hits	Search Text	DB	Time stamp
-	2	("5729126").PN.	USPAT; EPO; JPO; DERWENT	2002/10/02 11:13
-	2	("5802349").PN.	USPAT; EPO; JPO; DERWENT	2002/10/02 11:19
-	36208	((nois\$3 or glitch\$3) same (error\$3 or fail\$3 or fault\$3)) and ((integrated near1 circuit) or IC or VLSI or LSI or circuit)	USPAT; EPO; JPO; DERWENT	2002/10/03 12:54
-	13054	(buffer or repeater or inverter or inverter) and (((nois\$3 or glitch\$3) same (error\$3 or fail\$3 or fault\$3)) and ((integrated near1 circuit) or IC or VLSI or LSI or circuit))	USPAT; EPO; JPO; DERWENT	2002/10/02 11:35
-	2076	(noise near3 (voltage or threshold or max\$4)) and ((buffer or repeater or inverter or inverter) and (((nois\$3 or glitch\$3) same (error\$3 or fail\$3 or fault\$3)) and ((integrated near1 circuit) or IC or VLSI or LSI or circuit)))	USPAT; EPO; JPO; DERWENT	2002/10/02 11:40
-	439	(capacit\$4 near3 (cross-coupl\$3 or (cross near1 coupl\$3) or coupl\$3)) and ((noise near3 (voltage or threshold or max\$4)) and ((buffer or repeater or inverter or inverter) and (((nois\$3 or glitch\$3) same (error\$3 or fail\$3 or fault\$3)) and ((integrated near1 circuit) or IC or VLSI or LSI or circuit))))	USPAT; EPO; JPO; DERWENT	2002/10/03 11:02
-	0	agressor and victim and ((capacit\$4 near3 (cross-coupl\$3 or (cross near1 coupl\$3) or coupl\$3)) and ((noise near3 (voltage or threshold or max\$4)) and ((buffer or repeater or inverter or inverter) and (((nois\$3 or glitch\$3) same (error\$3 or fail\$3 or fault\$3)) and ((integrated near1 circuit) or IC or VLSI or LSI or circuit))))	USPAT; EPO; JPO; DERWENT	2002/10/02 11:47
-	4	aggressor and victim and ((capacit\$4 near3 (cross-coupl\$3 or (cross near1 coupl\$3) or coupl\$3)) and ((noise near3 (voltage or threshold or max\$4)) and ((buffer or repeater or inverter or inverter) and (((nois\$3 or glitch\$3) same (error\$3 or fail\$3 or fault\$3)) and ((integrated near1 circuit) or IC or VLSI or LSI or circuit))))	USPAT; EPO; JPO; DERWENT	2002/10/03 08:50
-	6	"6117182"	USPAT; EPO; JPO; DERWENT	2002/10/02 14:47
-	7	(aggressor or victim) and ((capacit\$4 near3 (cross-coupl\$3 or (cross near1 coupl\$3) or coupl\$3)) and ((noise near3 (voltage or threshold or max\$4)) and ((buffer or repeater or inverter or inverter) and (((nois\$3 or glitch\$3) same (error\$3 or fail\$3 or fault\$3)) and ((integrated near1 circuit) or IC or VLSI or LSI or circuit))))	USPAT; EPO; JPO; DERWENT	2002/10/03 13:37

-	8	(capacit\$4 near3 (cross-coupl\$3 or (cross near1 coupl\$3) or coupl\$3)) and ((noise near3 (voltage or threshold or max\$4)) and ((buffer or repeater or inverter or inverter) and ((nois\$3 or glitch\$3) same (error\$3 or fail\$3 or fault\$3)) and ((integrated near1 circuit) or IC or VLSI or LSI or circuit)))) and ((buffer or repeater or inverter or inverter) near3 (insert\$3 or interject\$3 or interpolat\$3 or interpos\$3 or introduc\$3))	USPAT; EPO; JPO; DERWENT	2002/10/03 12:52
-	3917	716/\$.ccls.	USPAT; EPO; JPO; DERWENT	2002/10/03 12:52
-	156	((buffer or repeater or inverter or inverter) near3 (insert\$3 or interject\$3 or interpolat\$3 or interpos\$3 or introduc\$3)) and 716/\$.ccls.	USPAT; EPO; JPO; DERWENT	2002/10/03 12:53
-	123	((buffer or repeater or inverter or inverter) near1 (insert\$3 or interject\$3 or interpolat\$3 or interpos\$3 or introduc\$3)) and 716/\$.ccls.	USPAT; EPO; JPO; DERWENT	2002/10/03 12:53
-	5	((nois\$3 or glitch\$3) same (error\$3 or fail\$3 or fault\$3)) and ((buffer or repeater or inverter or inverter) near3 (insert\$3 or interject\$3 or interpolat\$3 or interpos\$3 or introduc\$3)) and 716/\$.ccls.)	USPAT; EPO; JPO; DERWENT	2002/10/03 12:54
-	2	("6029117").PN.	USPAT; EPO; JPO; DERWENT	2002/10/03 13:37